

ABSTRACT OF THE DISCLOSURE

Input buffer type packet switching equipment, which can output cells to an external output line having a slower output line rate than a corresponding input line rate without having buffers in its output line sections, is provided. The input buffer type packet switching equipment provides M input line buffers that store cells inputted from M input lines temporarily in a state that one of the M input line buffers stores cells inputted from corresponding one of the M input lines, in this the M is an integer being 2 or more, an $M \times N$ crossbar type switch, which provides N output lines, for switching cells outputted from the M input line buffers based on a cross point on/off control signal, in this N is an integer being 2 or more. The input buffer type packet switching equipment further provides N output line sections, which are provided for each of the N output lines of the $M \times N$ crossbar type switch, for outputting cells applied switching at the $M \times N$ crossbar type switch to N external output lines, and an arbiter that outputs a connection permission signal to one of the M input line buffers based on connection request signals outputted from the M input line buffers, and also outputs the cross point on/off control signal to the $M \times N$ crossbar type switch, and outputs the connection permission signal at a designated slower timing interval than a normal timing interval to one input line buffer that outputs cells to an external output line whose output line rate is slower than a corresponding input line rate. With this, the timing interval outputting the cells from one of the M input line buffers to the $M \times N$ crossbar type switch is made to be wider than the normal timing interval, therefore buffers in the output line sections are not needed.